

## **ABSTRACT**

1        A kind of architecture of method for fetching microprocessor's instructions is  
2 provided to pre-read and pre-decode a next instruction. If the instruction  
3 pre-decoded is found a conditional branch instruction, an instruction  
4 reading-amount register is set for reading two instructions next to the current  
5 instruction in the program memory, or one is read instead if the next instruction is  
6 found an instruction other than the conditional branch one so as to waive reading of  
7 unnecessary program memory and thereby reduce power consumption.